FIG. 1

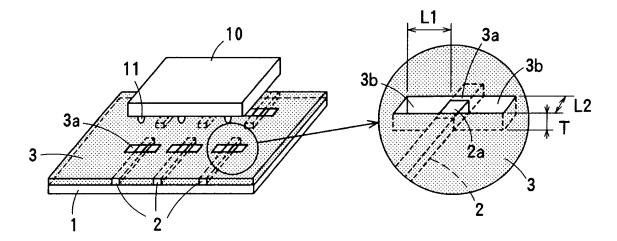
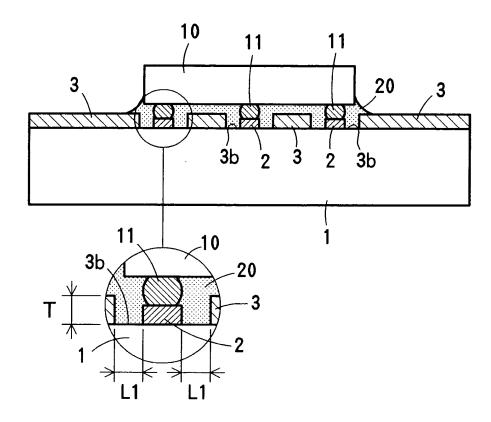


FIG. 2



Docket #: P/1071-1598

Title: MOUNTING BOARD AND ELECTRONIC

DEVICE USING THE SAME

Inventor: Hiroyuki ISHIZAKI Contact: James A. Finder (212) 382-0700

Drawings: 9 Sheets (Figs. 1-9)

### FIG. 3A

## STATE OF OCCURRENCE OF VOIDS (VISCOSITY: 0.4 Pa•s)

LONGITUDINAL LENGTH (L1)	INSULATOR THICKNESS (T)		OCCURRENCE OF VOIDS
(L1)	(T)	L1/T	
30 μ m	30 μ m	1.00	VOIDS OCCURRED
50 μ m	30 μ m	1.67	YOIDS OCCURRED
60 μ m	30 μ m	2.00	NO VOID
70 μ m	$30\mu$ m	2.33	NO YOID
<b>9</b> 0 μ m	30 μ m	3.00	NO VOID
30 μ m	50 μ m	0.60	VOIDS OCCURRED
<b>50</b> μ m	50 μ m	1.00	VOIDS OCCURRED
70 μ m	50 μ m	1.40	VOIDS OCCURRED
<b>90</b> μ m	$50\mu$ m	1.80	VOIDS OCCURRED
100 μ m	$50\mu$ m	2.00	NO AOID
$120\mu$ m	50 μ m	2.40	NO VOID
150 μ m	$50\mu\mathrm{m}$	3.00	dion on

# STATE OF OCCURRENCE OF VOIDS (VISCOSITY: 7.3 Pars)

FIG. 3B

LO	NGITUDINALLENGTH (L1)	INSULATOR THICKNESS (T)		OCCURRENCE OF VOIDS
	(L1)	(T)	L1/T	
	30 μ m	30 μ m	1.00	VOIDS OCCURRED
	50 μ m	30 μ m	1.67	VOIDS OCCURRED
	60 μ m	30 μ m	2.00	NO AOID
	70 μ m	30 μ m	2.33	No Aoid
	90 μ m	$30\mu$ m	3.00	NO AOID

FIG. 4

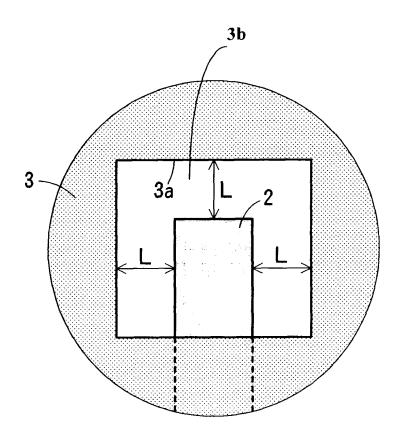


FIG. 5

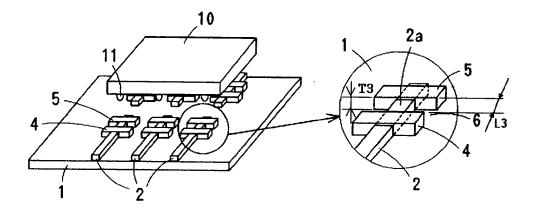


FIG. 6

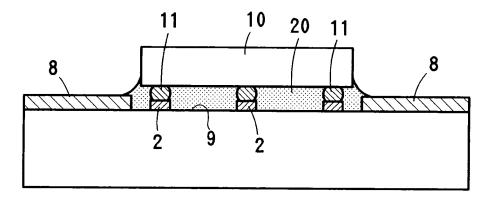
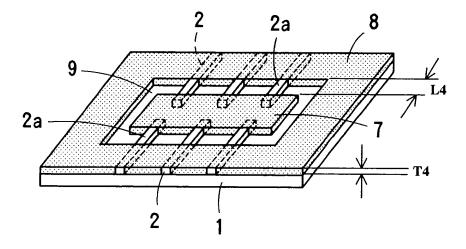
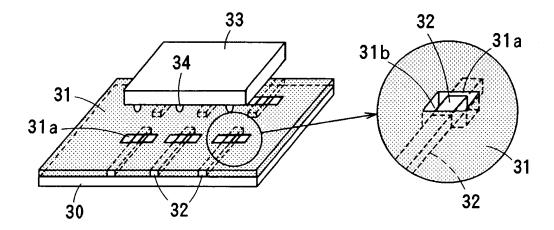


FIG. 7



### FIG. 8 **PRIOR ART**



#### FIG. 9 **PRIOR ART**

